

- integrated linear regulator, 382–386
  - LDO, 384
  - reducing power dissipation with resistor, 385–386
- regulator, 297
  - ripple, 388
  - ripple rejection, 374
  - switching type, 386–388
  - thermal analysis, *see* thermal analysis
    - defined, 267
    - reference, 295, 372–373, 379
    - ripple, 372
- von Neumann architecture, 149

## W

- wafer, semiconductor, 36
- wait state, 71, 125
- Watt, *see* power
- winding, transformer, *see* transformer

## wire

- delay matching in clock distribution, 358
- gauge and resistance, 391
- inductance, 276
- propagation delay, 359
- proximity and crosstalk, 408–409
  - transmission line, *see* transmission line
- wire-wrap circuit assembly, 427–428
- write enable (WE) signal, 65, 71, 87, 90

## X

- Xerox, 216
- Xilinx, 255, 258
- XON/XOFF, *see* handshaking

## Y

- Zener voltage, *see* diode
- Zilog, 122